

UNDERGRADUATES PURSUING RESEARCH IN SCIENCE AND ENGINEERING (UPRISE)

DEPARTMENT OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCE COLLEGE OF ENGINEERING AND APPLIED SCIENCE

SUMMER RESEARCH OPPORTUNITIES FOR UNDERGRADUATE students

APPLICATION DEADLINE: 02/25/2022

PROJECT TITLE: Deep-Learning-based Side-Channel Attacks

Boyang Wang
Department of Electrical Engineering and
Computer Science
806A Rhodes Hall
Cincinnati OH 45221
boyang.wang@uc.edu
https://homepages.uc.edu/~wang2ba/index.ht
ml

Project Description

A side-channel attack can infer the secret key on a device (e.g., a microcontroller, a secure chip on a credit card, or an IoT device) by analyzing power consumption when the device runs encryption algorithms, such as Advanced Standard Encryption. It is one of the primary threats to the security of embedded systems. While countermeasures (e.g., random delays, hiding, and masking) have been proposed to defend against traditional side-channel attacks, including Differential Power Analysis and Correlation Power Analysis, recent deep-learning-based side-channel attacks can defeat these existing countermeasures. Despite the promising results reported in recent studies, deep-learning-based side-channel attacks are not robust as they are sensitive to discrepancies between training data and test data. For instance, a deep neural network trained with data collected from one microcontroller (e.g., 8-bit XMEGA) may not derive high accuracy over data collected from another microcontroller (e.g., 8-bit XMEGA) due to minute changes in power measurements.

This project aims to enhance the robustness of deep-learning-based side-channel attacks. The students in this project will (1) Study research papers related to deep-learning-based side-channel attacks; (2) Explore new architectures of neural networks that can be more robust in side-channel attacks; (3) Learn cybersecurity and machine learning knowledge and skills related to this project; (4) Have access to GPU machines in Dr. Wang's lab for training neural networks; (5) Have access to data collection platform (Chipwhisperer) and corresponding microcontrollers in Dr. Wang's lab to collect power/EM traces of AES encryption for analysis.